

EUR  SPACE

DASIA 2014

THE INTERNATIONAL SPACE SYSTEM ENGINEERING CONFERENCE

JUNE 3

JUNE 5



WARSAWA

Programme


INTERCONTINENTAL
WARSAWA

in co-ordination with:





- 07:30 Registration & welcome coffee
- 09:30 DASIA 2014 introduction and two Keynotes:
 1. 'Space related activities in Poland' by Pawel Wojtkiewicz - Director of Polish Space Industry Association
 2. 'The specificities of Europe in Space' by Jean-Jacques Tortora - Eurospace Secretary General

Room A

A1 SYSTEM ENG. SUPPORT CHAIRED BY C. JOERGENSEN

- 10:30 **SW configuration management practises for an incremental space SW development process**
Massimo Tipaldi - OHB System AG, Germany / University of Sannio, Dept. of Engineering, Italy - Florian Regen, Jörg Brüggmann, Erik
- 11:00 **A data model framework for the characterization of a satellite data handling software**
Massimo Tipaldi - OHB System AG, Germany / University of Sannio, Dept. of Engineering, Italy - Gianluigi Camatto, Wolfgang Bothmer, Massimo Ferraguto, Bernhard Bruenjes - OHB System AG, Germany
- 11:30 **Critical Systems Engineering Accelerator: Aerospace Demonstrator**
Ricardo Moreno, Gonzalo Fernández, Raúl Regada, - Luis Basanta – Thales Alenia Space, Spain - Elena Alaña, María del Carmen Lomb - GMV Aerospace and Defence, Spain

12:15 LUNCH

A2 SYSTEM TREND ANALYSIS AND AUTOMATED TEST CHAIRED BY G. LAUTENSCHLAGER

- 13:30 **Trend and Health Management Analyzer (THEMA) : a step forward to an integrated analysis environment**
Annamaria Piras, Stefano Masiello, Giorgio Fasano – Thales Alenia Space, Italy
- 14:00 **Automated Source-code-based Testing of Object-Oriented Software**
Ralf Gerlich, Rainer Gerlich – BSSE, Germany - Carsten Dietrich – DLR, Germany

A3 DEPENDABILITY CHAIRED BY PH. LACAN

- 14:30 **Reference Architecture for High Dependability On-Board Computers**
Nuno Silva, Alexandre Esper, Ricardo Barbosa - Critical Software S.A., Portugal - Johan Zandin - RUAG Space AB, Sweden - Claudio Monteleone - ESA/ESTEC, The Netherlands

15:00 COFFEE BREAK

A3 DEPENDABILITY CHAIRED BY PH. LACAN

- 15:30 **Software failure propagation prevention: application of the guidelines and recommendations to proof of concept pilots for ground and flight software**
Mark Hann - SciSys UK Ltd, UK - Patricia Rodríguez Dapena – Software, Spain - David Moretti - ESA/ESTEC, The Netherlands

A4 FDIR CHAIRED BY PH. LACAN

- 16:00 **FAME Process: a dedicated development and V&V Process for FDIR**
Andrea Guiotto - Thales Alenia Space Italia, Italy - Regis Le Ferluc - Thales Alenia Space France, France - Marco Bozzano, Alessandro Cimatti, Marco Gario – Fondazione Bruno Kessler, Italy
- 16:30 **Development of a FDIR validation test-bed**
Jan Andersson, Daniel Cederman, Sandi Habinc – Aeroflex Gaisler, Sweden - Brice Dellandréa, Jean-Christian Nodet - Thales Alenia Space, France - Farid Guettache, Gianluca Furano – ESA/ESTEC, The Netherlands

A5 STANDARDIZING COMMUNICATION TO MOBILE & SPACEBORNE DEVICES CHAIRED BY R. GERLICH

- 17:00 **YETE: Distributed, Networked Embedded Control Approaches for Efficient, Reliable Mobile Systems**
Sergio Montenegro, Tobias Mikschl - Aerospace Information Technology University of Wür, Germany
- 17:30 **A communication paradigm for modular avionics: the DAPAR experience**
Roger Llorca-Cejudo, Jean-Louis Carayon – Centre National d'Etudes Spatiales, France



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Room B

B1 SAVOIR & MASS MEMORY CHAIRED BY U. HOCH

- 10:30 **SAVOIR & Mass Memory**
Ch. Honvault - ESA, The Netherlands
- 11:00 **NAND FLASH Radiation Tolerant Intelligent Memory Stack (RTIMS FLASH)**
Charles Sellier, Pierre Wang - 3D Plus, France
- 11:30 **SATA controller into a space CPU**
Maurizio de Nino, Giuseppe Capuano, Daniele Titomanlio, Raffaele Calvanese - Techno System developments, Italy

12:15 LUNCH

B1 SAVOIR & MASS MEMORY CHAIRED BY U. HOCH

- 13:30 **Versatile Rad-Hard Memory Controller**
Harald Michalik, Christian Dierker DSI, Germany - Torsten Fichna, Dietmar Walter, IDA, Technical University Braunschweig, Germany—Peleg Avieli, Tito Mandler, Fredy Lange, Ran Ginosar, Ramon Chips, Israel

B2 RAD HARD PROCESSORS AND DSPs CHAIRED BY H.-V. HEYER

- 14:00 **Atmel's New Rad-Hard Sparc V8 Processor 200Mhz & Low Power System on chip**
Nicolas Ganry, Guy Mantelet - Atmel Aerospace, France, - Steve Parkes and Chris McClements , Star Dundee, United Kingdom
- 14:30 **RC64, a rad-hard many-core high-performance DSP for space applications**
Ran Ginosar, Peleg Aviely, Tuvia Liran, Dov Alon, Alberto Mandler, Fredy Lange, Reuven Dobkin, Miki Goldberg - Ramon Chips, Israel

15:00 COFFEE BREAK

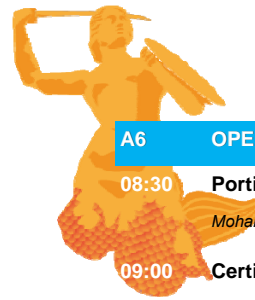
B3 GNSS & FPGAs CHAIRED BY K. MARINIS

- 15:30 **A family of new GNSS Receivers for Space Applications based on the AGGA-4**
Uwe Hoch- AIRBUS Defense & Space, Astrium GmbH, Germany
- 16:00 **Analysis of Design Alternatives on using Dynamic and Partial Reconfiguration in a Space Application**
Filip Veljković, Javier Mora, Teresa Riesgo, Eduardo de la Torre - Centro de Electrónica Industrial, ETSI Industriales, Universidad Politécnica de Madrid, Spain - Luis Berrojo Valero, Raúl Regada Alvarez, Ángel Álvaro Sánchez - Thales Alenia Space Spain

B4 PROCESSORS EMULATORS & SIMULATORS CHAIRED BY M. BLOMMENSTIJN

- 16:30 **An integrated SVF for real-time closed-loop high- complexity system simulations**
Juan Pérez, José A. Pulido, Pedro Palomo, Antonio Latorre - Deimos Space, Spain - João S. Silva, Hugo D. Lopes - Deimos Engenharia, Portugal - Alberto García – ESA/ESTEC, The Netherlands
- 17:00 **A LEON2&3 emulation board qualified for space programs based on reusable building blocks**
Grégory Quere, Frédéric Pasquier - Airbus Defence and Space, France - Mauro Caleno – ESA/ESTEC, The Netherlands
- 17:30 **Next Processor Module : a hardware accelerator of UT699 LEON3-FT system for On-Board Computer software simulation**
Serge Langlois, David Riant - Thales Alenia Space, France - Olivier Fouquet – Ingespace, France - Yann Gouy - AKKA I&S for Thales Alenia Space, France

Room A



A6 OPERATING SYSTEMS FOR MULTI-AND-SINGLE CORE CHAIRED BY M. HERNEK

08:30 Porting DubaiSat-2 Flight Software to RTEMS: A Feasibility Study
 Mohammed Khoory, Zakareyya Al Shamsi, Ibrahim Al Midfa - Emirates Institution for Advanced Science and Technology (EIAST), UAE

09:00 Certifying Applications for Safety and Security on a MultiCore platform
 Stuart Fisher, SYSGO, Germany

09:30 RTEMS SMP for LEON3/LEON4 Multi-Processor Devices
 Daniel Cederman, Daniel Hellström - Aeroflex Gaisler, Sweden—Joel Sherrill - OAR Corporation, USA - Gedare Bloom - Dept. of Computer Science, George Washington University, USA - Mathieu Patte - Airbus Defence and Space / Space Systems, USA - Marco Zulianello - ESA/ESTEC, The Netherlands

10:00 COFFEE BREAK

A6 OPERATING SYSTEMS FOR MULTI-AND-SINGLE CORE CHAIRED BY M. HERNEK

10:30 Multicore software architectures on virtualized partitioned systems
 E. Carrascosa, M. Masmano, P. Balbastre, A. Crespo - Instituto de Inform'atica Industrial, Universidad Polit'ecnica de Valencia, Spain - J. Gallizzi - Centre National d'Etudes Spatiales (CNES), France

A7 TIME PREDICTION WITH MULTICORE CHAIRED BY R. WARD

11:00 WCET and multicores with TSP
 Julien GALLIZZI, Fabien VIGEANT, Laurent PERRAUD - Centre National d'Etudes Spatiales, France - Alfons CRESPO, Miguel - MASMANO, Elena CARRASCOSA, Vicent BROCAL, Patricia BASTRE - FENTISS - Instituto - de Informatica Industrial, Universidad Polit'ecnica de Valencia - UPV, Spain - Fernand - QUARTIER - Spacebel, Belgium - Fabrice MILHORAT - CSSI, France

11:30 Temporal Partitioning on Multicore Platform
 Risat Mahmud Pathan, Feysal Hashi, Per Stenström Chalmers University of Technology, Sweden - Lars-Göran Green, Torbjörn Hult, Patrik Sandin RUAG Space Sweden AB, Sweden

12:15 LUNCH

A7 TIME PREDICTION WITH MULTICORE CHAIRED BY R. WARD

13:30 T-CREST: a time-predictable multi-core platform for aerospace applications
 Martin Schoeberl - Department of Applied Mathematics and Computer Science, Technical University of Denmark - Claudio Silva, Andre Rocha - GMV, Portugal

A8 TSP CERTIFICATION CHAIRED BY P. ARBERET

14:00 Formalisation of a separation micro-kernel for common criteria certification
 Andrew Butterfield, David Sanan - Lero. Trinity College, Ireland - Mike Hinchey - Lero. University of Limerick, Ireland

A9 MULTICORE & S/W MAINTENANCE WITH IMA CHAIRED BY P. ARBERET

14:30 Multima - Multi-core in integrated modular avionics
 Claudio Silva, Cassia Tatibana - GMV, Portugal

15:00 COFFEE BREAK

A9 MULTICORE & S/W MAINTENANCE WITH IMA CHAIRED BY P. ARBERET

15:30 Experimenting maintenance of flight software in an integrated modular avionics for space
 Johan Hardy, Thomas Laroche, Philippe Creten, Paul Parisi - Spacebel, Belgium - Martin Hiller - ESA/ESTEC, The Netherlands

16.30 WORKSHOP ON 'HIGH SPEED AND DETERMINISTIC LINKS' CHAIRED BY CH. HONVAULT
 1. Industrial view on constraints and requirements for selecting network types (pres. by Olivier Notebaert & Brice Dellandrea)
 2. ESA considerations for selection of network types (presentation by Jean-Francois Dufour)
 3. Discussion of proper network selection in view of resources and performance

Gala Dinner and Entertainment hosted by Eurospace

Meeting point and time: 7:00 pm in the lobby of the INTERCONTINENTAL hotel

Room B



B5 SPACE-WIRE & FIBER IMPLEMENTATION & TEST CHAIRED BY J-L. POUPAT

08:30 Developing and Testing SpaceWire Devices and Networks
 Steve Parkes, Stuart Mills - STAR-Dundee Ltd, United Kingdom

09:00 SpaceFibre Implementation, Test and Validation
 Steve Parkes, Chris McClements - University of Dundee, Space Technology Centre United Kingdom—Albert Ferre, Alberto Gonzalez Villafranca - STAR-Dundee Ltd, STAR House, United Kingdom

09:30 SpaceFibre Demonstrator (Demonstration and Testing)
 T. Helfers, P. Rastetter - Astrium GmbH, Germany - C. Papadas - Integrated Systems Development S.A., Greece - S. Parkes - University of Dundee, UK

10:00 COFFEE BREAK

B6 SPACE-WIRE & FIBER MANAGEMENT & PERFORMANCE CHAIRED BY O. NOTEBAERT

10:30 Network Management and FDIR for SpaceWire Networks (N-MaSS)
 Giuseppe Montano - Astrium Ltd., UK - David Jameux - ESA/ESTEC, The Netherlands - Barry Cook, Roger Peel, Ecaterina McCormick, Paul Walker - 4Links Ltd, UK - Vangelis Kollias4, Nikos Pogkas - Teletel S.A., Greece

11:00 MOST: Modeling of SpaceWire & SpaceFibre traffic
 Brice Dellandrea, Baptiste Gouin - Thales Alenia Space, France - Steve Parkes - University of Dundee, United Kingdom - David Jameux - ESA, The Netherlands

11:30 SpaceWire-D: Deterministic Data Delivery over SpaceWire
 Steve Parkes, David Gibson - University of Dundee, Space Technology Centre United Kingdom

12:15 LUNCH

B7 AFDX/TTE CHAIRED BY J-F DUFOUR

13:30 Towards a network centric distributed time- and space partitioned platform architecture
 C. Fidi, TTech Computertechnik AG, Austria - M. Jakovljevic - TTech AG, Germany - H.-J. Herpel, G. Willich - Astrium GmbH, Satellites, Germany

14:00 Maturation of the TTETHERNET technology for space applications
 C. Fidi, TTech Computertechnik AG, Austria - W. Steiner - TTech AG, Germany - A. Füsser, B. Wolff - Airbus Defense and Space, Germany

14:30 TTEthernet, a promising candidate for Ariane 6
 Rémi Clavier, Pierre Sautereau - Airbus Defence and Space, France - Jean-François Dufour - ESA/ESTEC, The Netherlands

15:00 COFFEE BREAK

B7 AFDX/TTE CHAIRED BY J-F DUFOUR

15:30 An AFDX Network for Spacecraft Data Handling
 Marie-Hélène Deredempt - Astrium SAS, Toulouse, France - Vangelis Kollias Teletel SA Athens, Greece - Zhili Sun - University of Surrey, UK - Ernest Canameres - GTD, Spain - Philippe Ricco - CES, Switzerland

16:00 µAFDX
 Bruno Pasquier - Airbus group Innovation, France - Iris Gaillardet - Airbus, France

16.30 WORKSHOP ON 'HIGH SPEED AND DETERMINISTIC LINKS' CHAIRED BY CH. HONVAULT in Room A
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JUNE 4TH, 2014

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Room A

A10 INTERFACING TO SENSORS & DRIVERS CHAIRED BY F. HASS

08:30 Fiber Bragg Grating Interrogator for Temperature measurement

Jesus Ortiz Martin, José González Torres – Astrium Crisa, Spain - Silvia Abad - FiberSensing, Sistemas Avançados de Monitorização, S.A., Portugal

09:00 Lightweight, Space And Power Efficient Solar Array Drive Design Implemented Within Remote Interface Unit

Tatu Kivikyto, Jyrki Laaksonen - PATRIA Aviation Oy, Systems, Finland

A11 CCSDS FILE CHAIRED BY CH. TAYLOR

09:30 CFDP configuration and simulation results. EUCLID and JUICE scenarios

Alberto Valverde, Chris Taylor, Juan Antonio Montesinos, Elena Maiorano, Cyril Colombo, Christian Erd, Giorgio Magistrati – ESA/ESTEC, The Netherlands

10:00 COFFEE BREAK

A11 CCSDS FILE CHAIRED BY CH. TAYLOR

10:30 CFDP: the revised standard and some handy lab tools

Juan Antonio Montesinos, Alberto Valverde, Chris Taylor, and Giorgio Magistrati – ESA/ ESTEC, The Netherlands

11:00 CFDP reference test facility (CRTF)

Stuart D. Fowell, Matt Jordan, Adrian Grenham - SCISYS UK Ltd, United Kingdom - Chris Taylor, Juan Antonio Montesinos - ESA/ESTEC, The Netherlands

11:30 File-based Operations and CFDP On-board Implementation

Ignacio Herrera Alzu, Alfonso Gonzalo Palomo, Francisco Perán Mazón - Airbus Defence and Space Crisa, Spain

12:15 LUNCH

A12 AUTONOMY & ROBOTICS CHAIRED BY A. MARTELLI

13:30 CRUISE – Evaluating Enhanced Crew Autonomy Concepts On-Board the ISS as a Preparation for Future Long Term Crewed Space Missions

Jasminka Matevska, Sonja Sievi (Presenter), Frank Plassmeier, Florian Gries - ASTRIUM Space Transportation GmbH, Germany - Mikael Wolff - ESA/ESTEC, The Netherlands - John Melody - SyberNet Ltd, Ireland

14:00 Capturing Requirements for Autonomous Spacecraft with Autonomy Requirements Engineering

Emil Vassev, Mike Hinchey - Lero – the Irish Software Engineering Research Centre, University of Limerick, Ireland

14:30 HRAF PILOT - prototype facility for verification & validation of robotics & autonomy

R. Ward, S. Reed – Scisys Ltd, United Kingdom - E. Monchieri – Airbus Defence and Space, United Kingdom - A. Martelli , A. Biggio – Thales Alenia Space Italy - H. Schroeven-Deceuninck – ESA: ECSAT, United Kingdom

15:00 COFFEE BREAK

A13 NAVIGATION & LANDING CHAIRED BY A. MARTELLI

15:30 A novel approach for Video-based Absolute Navigation in space exploration missions

Tadewos Getahun Tadewos, Paolo Prinetto, Daniele Rolfo, Pascal Trott - Politecnico di Torino, Dipartimento di Automatica e Informatica, Italy Piergiorgio Lanza, Andrea Martelli, Antonio Tramutola - Thales Alenia Space Italia S.p.a., Italy

16:00 A HW-SW CO-designed system for the lunar lander hazard detection and avoidance breadboarding

Pedro Palomo, Antonio Latorre, Carlos Valle, Sergio Gómez de Agüero, Miguel Hagenfeldt - Deimos Space, Spain - Baltazar Parreira - Deimos Engenharia, Portugal - Almudena Lindoso, Marta Portela, Mario Garcia, Enrique San Millán, Yuri Zharikov, Luis Entrena - Universidad Carlos III de Madrid, Spain

A14 SENSITIVE INFORMATION SHARING CHAIRED BY F. HASS

16:30 Toward the Specification of a Formal Framework for Sensitive Information Sharing

Eleftherios Skoutaris, Thomas Engel - Interdisciplinary Centre for Security, Reliability and Trust University of Luxembourg - Daniel Fischer - ESA / ESOC, Germany

A15 ON BOARD VIDEO PROCESSING CHAIRED BY F. HASS

17:00 GAIA video processing units from prototyping to in orbit operations

Pascal Paulet, Marc Le Roy - Airbus Defence & Space, France - Peter Bennie - Airbus Defence & Space, United Kingdom - Torgeir Paulsen – ESA/ESTEC, The Netherlands - Larry Longden, Robert Hillman - Maxwell Technologies, USA

Room B

B8 AVIONICS MODELING & SCALABILITY CHAIRED BY C. JOERGENSEN

08:30 Avionics architecture modelling language

Elena Alaña, Héctor Naranjo, Raúl Valencia, Alberto Medina -GMV Aerospace and Defence Spain - Christophe Honvault, Ana Rugina – ESA/ESTEC, The Netherlands - Marco Panunzio, Brice Dellandrea, Gerald Garcia - Thales Alenia Space France

09:00 Scalability of a Base Level Design for an On-Board-Computer for Scientific Missions

Carl Johann Treudler, Jan-Carsten Schröder, Fabian Greif, Kai Stohmann, Gökçe Aydos, Görschwin Fey - Department of Avionics Systems, Institute of Space Systems - German Aerospace Center (DLR), Germany

B9 FUTURE AVIONICS CHAIRED BY J. SCHNEEGANS

09:30 EXOMARS 2018 Avionic Architecture

Maurizio Caramia, Bruno Musetti – Thales Alenia Space, France - Eric Zekry – ESA/ESTEC, The Netherlands

10:00 COFFEE BREAK

B9 FUTURE AVIONICS CHAIRED BY J. SCHNEEGANS

10:30 Euclid Data Handling design solutions

Roberto Bogiatto, Antonio Tramutola, Corrado Maddaleno - Thales Alenia Space, Italy - Elena Maiorano, Cyril Colombo – ESA/ESTEC, The Netherlands

11:00 Instrument control unit for MERLIN

Heinz-Volker Heyer, Dr. Herbert Mosebach, Sven Semler, Dr. Klaus Raum - Kayser-Threde GmbH, Germany

11:30 Developing building blocks with space agencies: the keys to success

Jean-Luc Poupat - Airbus Defence and Space, France

12:15 LUNCH

B9 FUTURE AVIONICS CHAIRED BY J. SCHNEEGANS

13:30 High Performance COTS Based Computer

Mathieu Patte - Airbus Defense and Space / Space Systems, France - Raoul Grimoldi – OHB CGS, Germany

Roland Trautner – ESA/ESTEC, The Netherlands

B10 AVIONICS TECHNOLOGY & TEST CHAIRED BY B. MULLET

14:00 SOIS support for onboard device virtualisation and the use of electronic data sheets

Stuart D. Fowell, Richard Melvin - SCISYS UK Ltd, United Kingdom - Felice Torelli, Chris Taylor - ESA/ESTEC, The Netherlands

14:30 Evaluation of new European technologies for future avionics

Jean-Luc Poupat - Airbus Defence and Space, France - Laurent Chevalier - Airbus DS Space System, France - Georges-Albert Bonnerot - CNES DLA, France - Patrick Le Meur - CNES CS, France

15:00 COFFEE BREAK

B10 AVIONICS TECHNOLOGY & TEST CHAIRED BY B. MULLET

15:30 Suitability of the IEEE 802.15.4e extensions for spacecraft and launcher communications

Ph. Dallemagne, J.-D. Decotignie, D. Piquet – CSEM, Switzerland—P.Pelissou, M. Patte – ASTRIUM,France - J.-F. Dufour – ESAS/ESTEC, The Netherlands

16:00 The consolidation of the end-to-end avionics systemstestbench

Q.G.J. Wijnands – ESA/ESTEC/TEC-SWG, The Netherlands - F. Torelli – ESA/ESTEC/TEC-SWS, The Netherlands

16:30 iSAFT Protocol Validation Platform for on-board data networks

Antonis Tavoularis, Vangelis Kollias - TELETEL S.A., Greece - Kostas Marinis - TEC-EDD, ESA/ESTEC, The Netherlands

LOCATION

INTERCONTINENTAL WARSZAWA

EMILII PLATER STREET 49

WARSAW , 00-125

POLAND

FRONT DESK +48-22-3288888



REGISTRATION

Advance: Please complete the electronic Conference Registration Form (<http://eurospace.org/dasia-2014.aspx>) and send it to Eurospace before May 2nd, 2014

On site: The Conference Secretariat will open on June 3rd, 2014 for late registration, documentation and information, and will remain in operation throughout the Conference

CONFERENCE FEES

A Conference fee of **900 €** is requested from all participants, including speakers and chairmen. This fee will include lunches, coffee breaks, reception and proceedings.

The fee for a Vendor Display is **€2 000** and includes one attendance fee for the Conference.

PAYMENT

Before the Conference:

By cheque: in Euro to the order of EUROSPACE payable in a French Bank to be sent to:
EUROSPACE – 15/17, avenue de Ségur – 75007 PARIS, France.

By bank transfer: in Euro to **EUROSPACE – IBAN: FR76 3006 6100 9100 0103 6680 245 – B.I.C: CMCIFRPP**
C.I.C. Paris – Agence Bosquet – 16 ter avenue Bosquet – F-75007 PARIS.

Charges on bank orders shall be borne by the sender

In cash or credit card: In Euro **only** by cash, with a **VISA** or a **MASTERCARD** (AMERICAN EXPRESS will not be accepted)

CANCELLATIONS

Refunds of 50% will be made if a written request is received **before May 3rd, 2014 at the latest**. No refunds will be made for cancellations received after this date, but substitutions are possible.

HOTEL RESERVATIONS

A block reservation has been made at special Conference rates at INTERCONTINENTAL WARSZAWA.

It is recommended to reserve hotel accommodation **very rapidly** and at the latest before **March 31st, 2014** via the link provided in the **Eurospace website** (<http://eurospace.org/dasia-2014.aspx>)

Beyond that date, reservation will be on normal request and upon availability.

Important notice: The application of **DASIA 2014 negotiated rate(s)** is subject to the use of the order form specifically provided by **INTERCONTINENTAL WARSZAWA** available through the above link. **Please inform your travel agent if necessary.**

Once the booking confirmed, please contact the hotel directly regarding any subsequential question/change.

LANGUAGES

English will be the working language of the Conference. The time allocated for each presentation will include a discussion at the end

TRAVEL & BANK FACILITIES

Travel and **exchange** arrangements can be made by the Concierge of the Hotel.

PROCEEDINGS

The Proceedings will be published after the Conference :

On a CD Rom:

And via
Eurospace server:

Authors are requested to deliver the pdf version of their final paper(s) **by June 12th, 2014 at the latest**, via the following address exclusively: **dasia2014@eurospace.org**

CHAIRMAN & PROGRAMME COMMITTEE

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Conference Manager: N. Aleton, Eurospace

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J-C. Nodet, Thales Alenia Space

H. Pasquier, CNES

J.L. Poupat, Astrium Satellites

M. Richard-Foy, PrismTech

A. Rossignol, Astrium Satellites

J. Schneegans, Astrium Space Transportation GmbH

R. Ward, SciSys Ltd



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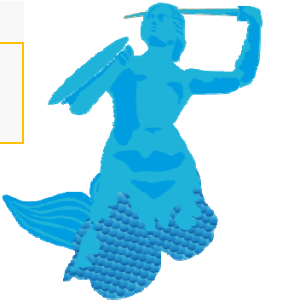
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- 17:00 **YETE: Distributed, Networked Embedded Control Approaches for Efficient, Reliable Mobile Systems**
 Sergio Montenegro, Tobias Mikschl - Aerospace Information Technology University of Wür, Germany
- 17:30 **A communication paradigm for modular avionics: the DAPAR experience**
 Roger Llorca-Cejudo, Jean-Louis Carayon – Centre National d'Etudes Spatiales, France



- 07:30 Registration & welcome coffee
- 09:30 DASIA 2014 introduction and two Keynotes in Room A:
 1. 'Space related activities in Poland' by Pawel Wojtkiewicz - Director of Polish Space Industry Association
 2. 'The specificities of Europe in Space' by Jean-Jacques Tortora - Eurospace Secretary General

Room B

B1 SAVOIR & MASS MEMORY CHAIRED BY U. HOCH

- 10:30 **SAVOIR & Mass Memory**
 Ch. Honvault - ESA, The Netherlands
- 11:00 **NAND FLASH Radiation Tolerant Intelligent Memory Stack (RTIMS FLASH)**
 Charles Sellier, Pierre Wang - 3D Plus, France
- 11:30 **SATA controller into a space CPU**
 Maurizio de Nino, Giuseppe Capuano, Daniele Titomanlio, Raffaele Calvanese - Techno System developments, Italy

12:15 LUNCH

B1 SAVOIR & MASS MEMORY CHAIRED BY U. HOCH

- 13:30 **Versatile Rad-Hard Memory Controller**
 Harald Michalik, Christian Dierker DSI, Germany - Torsten Fichna, Dietmar Walter, IDA, Technical University Braunschweig, Germany—Peleg Avieli, Tito Mandler, Fredy Lange, Ran Ginosar, Ramon Chips, Israel

B2 RAD HARD PROCESSORS AND DSPs CHAIRED BY H.-V. HEYER

- 14:00 **Atmel's New Rad-Hard Sparc V8 Processor 200Mhz & Low Power System on chip**
 Nicolas Ganry, Guy Mantelet - Atmel Aerospace, France, - Steve Parkes and Chris McClements, Star Dundee, United Kingdom
- 14:30 **RC64, a rad-hard many-core high-performance DSP for space applications**
 Ran Ginosar, Peleg Aviely, Tuvia Liran, Dov Alon, Alberto Mandler, Fredy Lange, Reuven Dobkin, Miki Goldberg - Ramon Chips, Israel

15:00 COFFEE BREAK

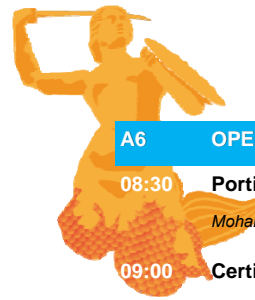
B3 GNSS & FPGAs CHAIRED BY K. MARINIS

- 15:30 **A family of new GNSS Receivers for Space Applications based on the AGGA-4**
 Uwe Hoch- AIRBUS Defense & Space, Astrium GmbH, Germany
- 16:00 **Analysis of Design Alternatives on using Dynamic and Partial Reconfiguration in a Space Application**
 Filip Veljković, Javier Mora, Teresa Riesgo, Eduardo de la Torre - Centro de Electrónica Industrial, ETSI Industriales, Universidad Politécnica de Madrid, Spain - Luis Berrojo Valero, Raúl Regada Alvarez, Ángel Álvaro Sánchez - Thales Alenia Space Spain

B4 PROCESSORS EMULATORS & SIMULATORS CHAIRED BY M. BLOMMENSTIJN

- 16:30 **An integrated SVF for real-time closed-loop high- complexity system simulations**
 Juan Pérez, José A. Pulido, Pedro Palomo, Antonio Latorre - Deimos Space, Spain - João S. Silva, Hugo D. Lopes - Deimos Engenharia, Portugal - Alberto García – ESA/ESTEC, The Netherlands
- 17:00 **A LEON2&3 emulation board qualified for space programs based on reusable building blocks**
 Grégory Quere, Frédéric Pasquier - Airbus Defence and Space, France - Mauro Caleno – ESA/ESTEC, The Netherlands
- 17:30 **Next Processor Module : a hardware accelerator of UT699 LEON3-FT system for On-Board Computer software simulation**
 Serge Langlois, David Riant - Thales Alenia Space, France - Olivier Fouquet – Ingespace, France - Yann Gouy - AKKA I&S for Thales Alenia Space, France

Room A



A6 OPERATING SYSTEMS FOR MULTI-AND-SINGLE CORE CHAIRED BY M. HERNEK

08:30 **Porting DubaiSat-2 Flight Software to RTEMS: A Feasibility Study**
 Mohammed Khoory, Zakareyya Al Shamsi, Ibrahim Al Midfa - Emirates Institution for Advanced Science and Technology (EIAST), UAE

09:00 **Certifying Applications for Safety and Security on a MultiCore platform**
 Stuart Fisher, SYSGO, Germany

09:30 **RTEMS SMP for LEON3/LEON4 Multi-Processor Devices**
 Daniel Cederman, Daniel Hellström - Aeroflex Gaisler, Sweden—Joel Sherrill - OAR Corporation, USA - Gedare Bloom - Dept. of Computer Science, George Washington University, USA - Mathieu Patte - Airbus Defence and Space / Space Systems, USA - Marco Zulianello - ESA/ESTEC, The Netherlands

10:00 COFFEE BREAK

A6 OPERATING SYSTEMS FOR MULTI-AND-SINGLE CORE CHAIRED BY M. HERNEK

10:30 **Multicore software architectures on virtualized partitioned systems**
 E. Carrascosa, M. Masmano, P. Balbastre, A. Crespo - Instituto de Inform'atica Industrial, Universidad Polit'ecnica de Valencia, Spain - J. Gallizzi - Centre National d'Etudes Spatiales (CNES), France

A7 TIME PREDICTION WITH MULTICORE CHAIRED BY R. WARD

11:00 **WCET and multicores with TSP**
 Julien GALLIZZI, Fabien VIGEANT, Laurent PERRAUD - Centre National d'Etudes Spatiales, France - Alfons CRESPO, Miguel - MASMANO, Elena CARRASCOSA, Vicent BROCAL, Patricia BASTRE - FENTISS - Instituto - de Informatica Industrial, Universidad Polit'ecnica de Valencia - UPV, Spain - Fernand - QUARTIER - Spacebel, Belgium - Fabrice MILHORAT - CSSI, France

11:30 **Temporal Partitioning on Multicore Platform**
 Risat Mahmud Pathan, Feysal Hashi, Per Stenström Chalmers University of Technology, Sweden - Lars-Göran Green, Torbjörn Hult, Patrik Sandin RUAG Space Sweden AB, Sweden

12:15 LUNCH

A7 TIME PREDICTION WITH MULTICORE CHAIRED BY R. WARD

13:30 **T-CREST: a time-predictable multi-core platform for aerospace applications**
 Martin Schoeberl - Department of Applied Mathematics and Computer Science, Technical University of Denmark - Claudio Silva, Andre Rocha - GMV, Portugal

A8 TSP CERTIFICATION CHAIRED BY P. ARBERET

14:00 **Formalisation of a separation micro-kernel for common criteria certification**
 Andrew Butterfield, David Sanan - Lero. Trinity College, Ireland - Mike Hinchey - Lero. University of Limerick, Ireland

A9 MULTICORE & S/W MAINTENANCE WITH IMA CHAIRED BY P. ARBERET

14:30 **Multima - Multi-core in integrated modular avionics**
 Claudio Silva, Cassia Tatibana - GMV, Portugal

15:00 COFFEE BREAK

A9 MULTICORE & S/W MAINTENANCE WITH IMA CHAIRED BY P. ARBERET

15:30 **Experimenting maintenance of flight software in an integrated modular avionics for space**
 Johan Hardy, Thomas Laroche, Philippe Creten, Paul Parisi - Spacebel, Belgium - Martin Hiller - ESA/ESTEC, The Netherlands

16.30 **WORKSHOP ON 'HIGH SPEED AND DETERMINISTIC LINKS' CHAIRED BY CH. HONVAULT**
 1. Industrial view on constraints and requirements for selecting network types (pres. by Olivier Notebaert & Brice Dellandrea)
 2. ESA considerations for selection of network types (presentation by Jean-Francois Dufour)
 3. Discussion of proper network selection in view of resources and performance

Gala Dinner and Entertainment hosted by Eurospace

Meeting point and time: 7:00 pm in the lobby of the INTERCONTINENTAL hotel

Room B



B5 SPACE-WIRE & FIBER IMPLEMENTATION & TEST CHAIRED BY J-L. POUPAT

08:30 **Developing and Testing SpaceWire Devices and Networks**
 Steve Parkes, Stuart Mills - STAR-Dundee Ltd, United Kingdom

09:00 **SpaceFibre Implementation, Test and Validation**
 Steve Parkes, Chris McClements - University of Dundee, Space Technology Centre United Kingdom—Albert Ferre, Alberto Gonzalez Villafranca - STAR-Dundee Ltd, STAR House, United Kingdom

09:30 **SpaceFibre Demonstrator (Demonstration and Testing)**
 T. Helfers, P. Rastetter - Astrium GmbH, Germany - C. Papadas - Integrated Systems Development S.A., Greece - S. Parkes - University of Dundee, UK

10:00 COFFEE BREAK

B6 SPACE-WIRE & FIBER MANAGEMENT & PERFORMANCE CHAIRED BY O. NOTEBAERT

10:30 **Network Management and FDIR for SpaceWire Networks (N-MaSS)**
 Giuseppe Montano - Astrium Ltd., UK - David Jameux - ESA/ESTEC, The Netherlands - Barry Cook, Roger Peel, Ecaterina McCormick, Paul Walker - 4Links Ltd, UK - Vangelis Kollias4, Nikos Pogkas - Teletel S.A., Greece

11:00 **MOST: Modeling of SpaceWire & SpaceFibre traffic**
 Brice Dellandrea, Baptiste Gouin - Thales Alenia Space, France - Steve Parkes - University of Dundee, United Kingdom - David Jameux - ESA, The Netherlands

11:30 **SpaceWire-D: Deterministic Data Delivery over SpaceWire**
 Steve Parkes, David Gibson - University of Dundee, Space Technology Centre United Kingdom

12:15 LUNCH

B7 AFDX/TTE CHAIRED BY J-F DUFOUR

13:30 **Towards a network centric distributed time- and space partitioned platform architecture**
 C. Fidi, TTTech Computertechnik AG, Austria - M. Jakovljevic - TTTech AG, Germany - H.-J. Herpel, G. Willich - Astrium GmbH, Satellites, Germany

14:00 **Maturation of the TTETHERNET technology for space applications**
 C. Fidi, TTTech Computertechnik AG, Austria - W. Steiner - TTTech AG, Germany - A. Füsser, B. Wolff - Airbus Defense and Space, Germany

14:30 **TTEthernet, a promising candidate for Ariane 6**
 Rémi Clavier, Pierre Sautereau - Airbus Defence and Space, France - Jean-François Dufour - ESA/ESTEC, The Netherlands

15:00 COFFEE BREAK

B7 AFDX/TTE CHAIRED BY J-F DUFOUR

15:30 **An AFDX Network for Spacecraft Data Handling**
 Marie-Hélène Deredempt - Astrium SAS, Toulouse, France - Vangelis Kollias Teletel SA Athens, Greece - Zhili Sun - University of Surrey, UK - Ernest Canameres - GTD, Spain - Philippe Ricco - CES, Switzerland

16:00 **µAFDX**
 Bruno Pasquier - Airbus group Innovation, France - Iris Gaillardet - Airbus, France

16.30 **WORKSHOP ON 'HIGH SPEED AND DETERMINISTIC LINKS' CHAIRED BY CH. HONVAULT in Room A**
 1. Industrial view on constraints and requirements for selecting network types (pres. by Olivier Notebaert & Brice Dellandrea)
 2. ESA considerations for selection of network types (presentation by Jean-Francois Dufour)
 3. Discussion of proper network selection in view of resources and performance

Gala Dinner and Entertainment hosted by Eurospace

Meeting point and time: 7:00 pm in the lobby of the INTERCONTINENTAL hotel

Room A

A10 INTERFACING TO SENSORS & DRIVERS CHAIRED BY F. HASS

08:30 Fiber Bragg Grating Interrogator for Temperature measurement

Jesus Ortiz Martin, José González Torres – Astrium Crisa, Spain - Silvia Abad - FiberSensing, Sistemas Avançados de Monitorização, S.A., Portugal

09:00 Lightweight, Space And Power Efficient Solar Array Drive Design Implemented Within Remote Interface Unit

Tatu Kivikyto, Jyrki Laaksonen - PATRIA Aviation Oy, Systems, Finland

A11 CCSDS FILE CHAIRED BY CH. TAYLOR

09:30 CFDP configuration and simulation results. EUCLID and JUICE scenarios

Alberto Valverde, Chris Taylor, Juan Antonio Montesinos, Elena Maiorano, Cyril Colombo, Christian Erd, Giorgio Magistrati – ESA/ESTEC, The Netherlands

10:00 COFFEE BREAK

A11 CCSDS FILE CHAIRED BY CH. TAYLOR

10:30 CFDP: the revised standard and some handy lab tools

Juan Antonio Montesinos, Alberto Valverde, Chris Taylor, and Giorgio Magistrati – ESA/ ESTEC, The Netherlands

11:00 CFDP reference test facility (CRTF)

Stuart D. Fowell, Matt Jordan, Adrian Grenham - SCISYS UK Ltd, United Kingdom - Chris Taylor, Juan Antonio Montesinos - ESA/ESTEC, The Netherlands

11:30 File-based Operations and CFDP On-board Implementation

Ignacio Herrera Alzu, Alfonso Gonzalo Palomo, Francisco Perán Mazón - Airbus Defence and Space Crisa, Spain

12:15 LUNCH

A12 AUTONOMY & ROBOTICS CHAIRED BY A. MARTELLI

13:30 CRUISE – Evaluating Enhanced Crew Autonomy Concepts On-Board the ISS as a Preparation for Future Long Term Crewed Space Missions

Jasminka Matevska, Sonja Sievi (Presenter), Frank Plassmeier, Florian Gries - ASTRIUM Space Transportation GmbH, Germany - Mikael Wolff - ESA/ESTEC, The Netherlands - John Melody - SyberNet Ltd, Ireland

14:00 Capturing Requirements for Autonomous Spacecraft with Autonomy Requirements Engineering

Emil Vashev, Mike Hinchey - Lero – the Irish Software Engineering Research Centre, University of Limerick, Ireland

14:30 HRAF PILOT - prototype facility for verification & validation of robotics & autonomy

R. Ward, S. Reed – Scisys Ltd, United Kingdom - E. Monchieri – Airbus Defence and Space, United Kingdom - A. Martelli , A. Biggio – Thales Alenia Space Italy - H. Schroeven-Deceuninck – ESA: ECSAT, United Kingdom

15:00 COFFEE BREAK

A13 NAVIGATION & LANDING CHAIRED BY A. MARTELLI

15:30 A novel approach for Video-based Absolute Navigation in space exploration missions

Tadewos Getahun Tadewos, Paolo Prinetto, Daniele Rolfo, Pascal Trott - Politecnico di Torino, Dipartimento di Automatica e Informatica, Italy Piergiorgio Lanza, Andrea Martelli, Antonio Tramutola - Thales Alenia Space Italia S.p.a., Italy

16:00 A HW-SW CO-designed system for the lunar lander hazard detection and avoidance breadboarding

Pedro Palomo, Antonio Latorre, Carlos Valle, Sergio Gómez de Agüero, Miguel Hagenfeldt - Deimos Space, Spain - Baltazar Parreira - Deimos Engenharia, Portugal - Almudena Lindoso, Marta Portela, Mario Garcia, Enrique San Millán, Yuri Zharikov, Luis Entrena - Universidad Carlos III de Madrid, Spain

A14 SENSITIVE INFORMATION SHARING CHAIRED BY F. HASS

16:30 Toward the Specification of a Formal Framework for Sensitive Information Sharing

Eleftherios Skoutaris, Thomas Engel - Interdisciplinary Centre for Security, Reliability and Trust University of Luxembourg - Daniel Fischer - ESA / ESOC, Germany

A15 ON BOARD VIDEO PROCESSING CHAIRED BY F. HASS

17:00 GAIA video processing units from prototyping to in orbit operations

Pascal Paulet, Marc Le Roy - Airbus Defence & Space, France - Peter Bennie - Airbus Defence & Space, United Kingdom - Torgeir Paulsen – ESA/ESTEC, The Netherlands - Larry Longden, Robert Hillman - Maxwell Technologies, USA

Room B

B8 AVIONICS MODELING & SCALABILITY CHAIRED BY C. JOERGENSEN

08:30 Avionics architecture modelling language

Elena Alaña, Héctor Naranjo, Raúl Valencia, Alberto Medina -GMV Aerospace and Defence Spain - Christophe Honvault, Ana Rugina – ESA/ESTEC, The Netherlands - Marco Panunzio, Brice Dellandrea, Gerald Garcia - Thales Alenia Space France

09:00 Scalability of a Base Level Design for an On-Board-Computer for Scientific Missions

Carl Johann Treudler, Jan-Carsten Schröder, Fabian Greif, Kai Stohmann, Gökçe Aydos, Görschwin Fey - Department of Avionics Systems, Institute of Space Systems - German Aerospace Center (DLR), Germany

B9 FUTURE AVIONICS CHAIRED BY J. SCHNEEGANS

09:30 EXOMARS 2018 Avionic Architecture

Maurizio Caramia, Bruno Musetti – Thales Alenia Space, France - Eric Zekry – ESA/ESTEC, The Netherlands

10:00 COFFEE BREAK

B9 FUTURE AVIONICS CHAIRED BY J. SCHNEEGANS

10:30 Euclid Data Handling design solutions

Roberto Bogiatto, Antonio Tramutola, Corrado Maddaleno - Thales Alenia Space, Italy - Elena Maiorano, Cyril Colombo – ESA/ESTEC, The Netherlands

11:00 Instrument control unit for MERLIN

Heinz-Volker Heyer, Dr. Herbert Mosebach, Sven Semler, Dr. Klaus Raum - Kayser-Threde GmbH, Germany

11:30 Developing building blocks with space agencies: the keys to success

Jean-Luc Poupat - Airbus Defence and Space, France

12:15 LUNCH

B9 FUTURE AVIONICS CHAIRED BY J. SCHNEEGANS

13:30 High Performance COTS Based Computer

Mathieu Patte - Airbus Defense and Space / Space Systems, France - Raoul Grimoldi – OHB CGS, Germany

Roland Trautner – ESA/ESTEC, The Netherlands

B10 AVIONICS TECHNOLOGY & TEST CHAIRED BY B. MULLET

14:00 SOIS support for onboard device virtualisation and the use of electronic data sheets

Stuart D. Fowell, Richard Melvin - SCISYS UK Ltd, United Kingdom - Felice Torelli, Chris Taylor - ESA/ESTEC, The Netherlands

14:30 Evaluation of new European technologies for future avionics

Jean-Luc Poupat - Airbus Defence and Space, France - Laurent Chevalier - Airbus DS Space System, France - Georges-Albert Bonnerot - CNES DLA, France - Patrick Le Meur - CNES CS, France

15:00 COFFEE BREAK

B10 AVIONICS TECHNOLOGY & TEST CHAIRED BY B. MULLET

15:30 Suitability of the IEEE 802.15.4e extensions for spacecraft and launcher communications

Ph. Dallemagne, J.-D. Decotignie, D. Piquet – CSEM, Switzerland—P.Pelissou, M. Patte – ASTRIUM,France - J.-F. Dufour – ESAS/ESTEC, The Netherlands

16:00 The consolidation of the end-to-end avionics systemstestbench

Q.G.J. Wijnands – ESA/ESTEC/TEC-SWG, The Netherlands - F. Torelli – ESA/ESTEC/TEC-SWS, The Netherlands

16:30 iSAFT Protocol Validation Platform for on-board data networks

Antonis Tavoularis, Vangelis Kollias - TELETEL S.A., Greece - Kostas Marinis - TEC-EDD, ESA/ESTEC, The Netherlands

LOCATION

INTERCONTINENTAL WARSZAWA

EMILII PLATER STREET 49

WARSAW , 00-125

POLAND

FRONT DESK +48-22-3288888



REGISTRATION

Advance: Please complete the electronic Conference Registration Form (<http://eurospace.org/dasia-2014.aspx>) and send it to Eurospace before May 2nd, 2014

On site: The Conference Secretariat will open on June 3rd, 2014 for late registration, documentation and information, and will remain in operation throughout the Conference

CONFERENCE FEES

A Conference fee of **900 €** is requested from all participants, including speakers and chairmen. This fee will include lunches, coffee breaks, reception and proceedings.

The fee for a Vendor Display is **€2 000** and includes one attendance fee for the Conference.

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Before the Conference:

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Refunds of 50% will be made if a written request is received **before May 3rd, 2014 at the latest**. No refunds will be made for cancellations received after this date, but substitutions are possible.

HOTEL RESERVATIONS

A block reservation has been made at special Conference rates at INTERCONTINENTAL WARSZAWA.

It is recommended to reserve hotel accommodation **very rapidly** and at the latest before **March 31st, 2014** via the link provided in the **Eurospace website** (<http://eurospace.org/dasia-2014.aspx>)

Beyond that date, reservation will be on normal request and upon availability.

Important notice: The application of **DASIA 2014 negotiated rate(s)** is subject to the use of the order form specifically provided by **INTERCONTINENTAL WARSZAWA** available through the above link. **Please inform your travel agent if necessary.**

Once the booking confirmed, please contact the hotel directly regarding any subsequential question/change.

LANGUAGES

English will be the working language of the Conference. The time allocated for each presentation will include a discussion at the end

TRAVEL & BANK FACILITIES

Travel and **exchange** arrangements can be made by the Concierge of the Hotel.

PROCEEDINGS

The Proceedings will be published after the Conference :

On a CD Rom:

And via
Eurospace server:

Authors are requested to deliver the pdf version of their final paper(s) **by June 12th, 2014 at the latest**, via the following address exclusively: **dasia2014@eurospace.org**

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