

EUR  SPACE



DASIA 2015

The International Space System Engineering Conference



Barcelona

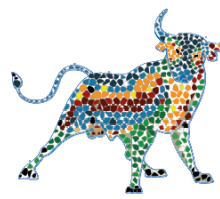
May 19 - May 21



BARCELONA SKIPPER

in co-ordination with:



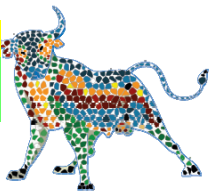


07:30 Registration & welcome coffee
09:00 DASIA 2015 : Opening by F.HASS and Presentation of Status of Spanish Space activities by J.C.CORTEZ - CDTI

Room A

A1	THE ROSETTA MISSION - CHAIRED BY F. HASS
09:45	ROSETTA – Mastering Challenges in Space <i>Gunther Lautenschläger - Rosetta team, Airbus DS GmbH, Germany</i>
A2	PAYLOAD PROCESSORS & ASSOCIATED - CHAIRED BY G. MAGISTRATI
11:00	Development of new European VLIW SPACE DSP ASICS, IP cores and related software via ESA contracts in 2015 and beyond <i>Roland Trautner – ESA/ESTEC, The Netherlands</i>
11:30	65nm RadSafe™ technology for RC64 and advanced SOCs <i>Tuvia Liran, Ran Ginosar, Fredy Lange, Alberto Mandler, Peleg Avieli, Henri Meirov, Michael Goldberg, Zeev Maister, Mickey Oliel – Ramon Chips, Israel</i>
12:00	RC64, a rad-hard many-core high-performance DSP for space applications <i>Ran Ginosar, Peleg Avieli, Hagay Gellis, Tuvia Liran, Tsvika Israeli, Roy Neshet, Fredy Lange, Reuven Dobkin, Henri Meirov, Dror Reznik - Ramon-Chips Ltd., Israel</i>
12:30	LUNCH
A2	PAYLOAD PROCESSORS & ASSOCIATED - CHAIRED BY G. MAGISTRATI
14:00	Scalable sensor data processor: a multi-core payload data processor ASIC <i>L. Berrojo, R. Moreno, R. Regada, E. García, - Thales Alenia Space, Spain - R. Trautner – ESA/ESTEC, The Netherlands - G. Rauwerda, K. Sunesen - Recore Systems BV – The Netherlands - S. Redant, G. Thys – IMEC, Belgium - J. Andersson, S. Habinc – Cobham Gaisler, Sweden</i>
A3	OB MICRO-CONTROLLERS & PROCESSORS - CHAIRED BY H-V. HEYER
14:30	GR740: Rad-Hard Quad-Core LEON4FT System-on-Chip <i>Magnus Hjorth, Martin Åberg, Nils-Johan Wessman, Jan Andersson – Cobham Gaisler, Sweden - Remy Chevallier - STMicroelectronics, France - Russel Forsyth – STMicroelectronics, United Kingdom - Roland Weigand, Luca Fossati – ESA/ESTEC, The Netherlands</i>
15:00	Rad-Hard Microcontroller For Space Applications <i>Sandi Habinc, Fredrik Johansson, Fredrik Sturesson, Martin Simlask, Magnus Hjorth, Jan Andersson - Cobham Gaisler, Sweden - Steven Redant, Wim Sijbers, Geert Thys – IMEC, Belgium - Claudio Monteleone - ESA/ESTEC, The Netherlands</i>
15:30	Porting Leon3ft/Grlib to 4th generation flash-based devices <i>Jan Andersson, Sandi Habinc -Cobham Gaisler, Sweden - Paolo Prinetto, Pascal Trotta - Politecnico di Torino, Dipartimento di Automatica e Informatica, Italy</i>
16:00	COFFEE BREAK
A3	OB MICRO-CONTROLLERS & PROCESSORS - CHAIRED BY H-V. HEYER
16:30	ARM for Platform Application <i>Matthieu Patte - Airbus DS Space System, France - Jean-Luc Poupat - Airbus DS Electronics, France - Patrick LeMeur – CNES, France</i>
17:00	TCLS arm for space <i>Benoit Leroy, Tim Helfers - Airbus DS Electronics, Germany - Jean-Luc Poupat - Airbus DS Electronics, France</i>
A4	HI-REL MEMORY & MASS STORAGE - CHAIRED BY G. ESTAVES
17:30	3D PLUS HI-REL DDR2 termination regulator module” “A Building Block function for High Reliability SDRAM DDR2 System Architecture” <i>Charles Sellier, Nicolas Perrot, Esther Garcia Sanchez, Patrick Dubus - 3D PLUS, France</i>
18:00	A Fault-Tolerant Radiation-Robust Mass Storage Concept for Highly Scaled Flash Memory <i>Christian M. Fuchs# Institute for Astronautic, Chair for Computer Architecture and Organization, Technical University Munich, Germany - Carsten Trinitis - Chair for Computer Architecture and Organization, Technical University Munich, Germany - Nicolas Appel, Martin Langer, Institute for Astronautic, Germany</i>
A5	FDIR & FLEXIBLE FAULT HANDLING - CHAIRED BY R. WARD
18:30	FDIR validation test-bed development and results <i>Alexander Karlsson, Anandhavel Sakthivel, Martin Åberg, Jan Andersson, Sandi Habinc - Cobham Gaisler, Sweden - Brice Dellandréa, Jean-Christian Nodet – Thales Alenia Space, France - Farid Guettache, Gianluca Furano - ESA/ESTEC, The Netherlands</i>

May 19th, 2015



07:30 Registration & welcome coffee
09:00 DASIA 2015 in Room A: Opening by F.HASS and Presentation of Status of Spanish Space activities by J.C.CORTEZ - CDTI
 Internacional Programmes Director

Room B

A1	In Room A: THE ROSETTA MISSION - CHAIRED BY FINN HASS
09:45	ROSETTA – Mastering Challenges in Space <i>Gunther Lautenschläger - Rosetta team, Airbus DS GmbH, Germany</i>
B1	SYSTEM ENGINEERING TOOLS INCL. MODELING & SIMULATION - CHAIRED BY C. JÖRGENSEN
11:00	System modeling & simulation tools_ESA Roadmap Status <i>Quirien Wijnands, Robert Blommestijn - ESA/ESTEC, The Netherlands</i>
11:30	Model Based System Engineering with Capella <i>Gerald Bourgeois, Thierry Neige, Hervé Bratulic, R. de Ferluc, F. Bergomi, Antoine Provost-Grellier- Thales Alenia Space, France</i>
B2	FLIGHT S/W ARCHITECTURES & SYSTEMS - CHAIRED BY G. LAUTENSCHLÄGER
12:00	On-board Software Reference Architecture for Payloads <i>Victor Bos, - SSF, Finland - Adam Trcka, - Evolsys, Czech Republic</i>
12:30	LUNCH
B2	FLIGHT S/W ARCHITECTURES & SYSTEMS - CHAIRED BY G. LAUTENSCHLÄGER
14:00	ADA, the programming language of choice for the UPMSAT-2 satellite <i>Jorge Garrido, Juan Zamorano, Juan Antonio de la Puente, Alejandro Alonso, Emilio Salazar - Universidad Politecnica de Madrid (UPM), Spain</i>
14:30	An LLVM backend for LEON processors <i>Jorge Lopez Trescastro - ESA/ESTEC, The Netherlands - Emil Vassev - Lero - the Irish Software Research Centre, University of Limerick, United Kingdom - Daniel Hellstrom, Daniel Cederman - Cobham Gaisler, Sweden</i>
15:00	Flight Software Development for the CHEOPS Instrument with the CORDET Framework <i>V. Cechticky, A. Pasetti - P&P Software GmbH, Switzerland - R. Ottensamer - University of Vienna, Austria</i>
15:30	Aspects of applying an object oriented design to a small satellite flight software <i>Ulrich Mohr, Bastian Baetz, - IRS, University of Stuttgart, Germany - Jens Eickhoff - Airbus DS GmbH, Germany - Sabine Klinkner - IRS, University of Stuttgart, Germany</i>
16:00	COFFEE BREAK
B2	FLIGHT S/W ARCHITECTURES & SYSTEMS - CHAIRED BY G. LAUTENSCHLÄGER
16:30	A Consistent Avionics Computing Platform for Spacecraft Control and Data Processing <i>Jens Eickhoff - Airbus DS GmbH & Institute of Space Systems, Universität Stuttgart, Germany - Hans-Jürgen Herpel, Georg Willich - Airbus DS GmbH, Germany</i>
17:00	Porting DubaiSat-2 Flight Software to RTEMS: A Feasibility Study <i>Mohammed Khoory, Zakareyya Al Shamsi, - Ibrahim Al Midfa - Emirates Institution for Advanced Science and Technology (EIAST), UAE</i>
B3	IMAGING, VISION & GNSS Rx BASED NAVIGATION - CHAIRED BY A. MARTELLI
17:30	Imaging Systems for Planetary Exploration <i>Ekkehard Kührt, Alexander Lichopoj, Harald Ichaelis, Stefano Mottola, Thomas Behnke, Rolf Schrödter, Matthias Tschentscher - Deutsches Zentrum für Luft- und Raumfahrt (DLR), Germany</i>
18:00	Multi-purpose Avionic Architecture for Vision based Navigation systems for EDL and Surface Mobility scenarios <i>A. Tramutola, D.Paltro - Thales Alenia Space Italia, Italy - M. P. Cabalo Perucha, G.Paar, J. Steiner - Joanneum Research, Austria - A.M.Barrio - ESA/ESTEC, The Netherlands</i>
18:30	The LION Navigator_A family of new GNSS Receivers for Space Applications_ based on the AGGA-4_Status and Achievements <i>Uwe Hoch - AIRBUS Defense & Space GmbH, Germany</i>

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Room A

A5 FDIR & FLEXIBLE FAULT HANDLING - CHAIRED BY R. WARD

- 08:30 Flexible approaches to fault-tolerant microprocessors for space applications**
L. Entrena, M. Portela, A. Lindoso, M. García-Valderas, L. Mengibar, L. Parra - Universidad Carlos III de Madrid, Spain—J. A. Pulido, A. Latorre – Deimos Space, Spain
- 09:00 High Available COTS Based Computer for Space**
J. Hartmann / Airbus Defence and Space GmbH, Germany - Giorgio Magistrati (TEC-EDD) / ESA ESTEC , The Netherlands

A6 QUALIFYING IMA TSP KERNELS - CHAIRED BY G. FURANO

- 09:30 A robustness testing campaign for IMA-SP partitioning kernels**
Stephen Grixti , Nicholas Sammut, David Zammit-Mangion – University of Malta - Jorge Lopez Trecastro, ESA/ESTEC, The Netherlands
- 10:00 Towards the adoption of formal techniques for kernel qualification**
Andrew Butterfield - Lero. Trinity College Dublin, Ireland—Mike Hinchey - Lero. University of Limerick, Ireland

10:30 COFFEE BREAK

A6 QUALIFYING IMA TSP KERNELS - CHAIRED BY G. FURANO

- 11:00 Requirements baseline for integrated modular avionics for space separation kernel qualification**
Mark Hann - SciSys UK Ltd, United Kingdom - Marie H el ene Deredempt, Alexandre Cortier - Airbus Defence and Space, France - R gis De Ferluc – Thales Alenia Space, France

A7 IMA-TSP SERVICES - CHAIRED BY U. HOCH

- 11:30 LVCUGEN – ready for flight?**
Julien Galizzi – CNES, France

- 12:00 System design toolkit for integrated modular avionics for space**
Mark Hann - SciSys UK Ltd, United Kingdom - Patricia Balbastre Betoret , Jos e Enrique Simo Ten - Fent Innovative Software Solutions, Spain -R gis De Ferluc - Thales Alenia Space France - Jinesh Ramachandran - ESA/ESTEC - The Netherlands

12:30 LUNCH

A7 IMA-TSP SERVICES - CHAIRED BY U. HOCH

- 14:00 Generic IO Controller Design for Time and Space Partitioning Architecture**
J r me Lachaize, Marie-H el ene Deredempt - Airbus Defence and Space SAS, France—Julien Galizzi – CNES, France

A8 PARTITIONING & MULTI-CORE PROCESSING - CHAIRED BY J-L. POUPAT

- 14:30 Integrated payload data handling systems using software partitioning**
Alun Taylor, Mark Hann -SciSys UK Ltd, United Kingdom - Alex Wishart - Airbus Defence and Space Ltd, United Kingdom
- 15:00 RTEMS SMP and MTAPI for Efficient Multi-Core Space Applications on LEON3/LEON4 Processors**
Daniel Cederman, Daniel Hellstr m – Cobham Gaisler,Sweden - Joel Sherrill - OAR Corporation, USA - Gedare Bloom - Dept. of Computer Science, George Washington University, USA - Matthieu Patte – Airbus Defence & Space SAS, France - Marco Zulianello – ESA/ESTEC, The Netherlands
- 15:30 Approaching parallelization of payload software applications on ARM multicore platforms**
Pierre Bretault, Nicolas Chatonnay, Brigitte Calmet - Thales Alenia Space, France

16:00 COFFEE BREAK

16.30 - 18:00 **CYBERSPACE SECURITY**
 Presentations and discussion in forum

Gala Dinner and Entertainment hosted by Eurospace
Meeting point and time: 7:00 pm in the lobby of the PULLMAN SKIPPER hotel



Room B

B4 OB IMAGE INTERPRETING & DATA CATALOGING - CHAIRED BY A. MARTELLI

- 08:30 Euclid Star catalogue management for the fine guidance sensor**
A. Bosco, A. Bacchetta – Thales Alenia Space, Italy - G. Saavedra Criado – ESA/ESTEC, The Netherlands - D. Procopio – Selex-ES, Italy - E.Palombo - Segula Technologies, Italy
- 09:00 QI2S - Quick Image Interpretation System**
Jamin Naghmouchi - Technische Universitaet Braunschweig, Germany - Peleg Aviely, Ran Ginosar - Ramon Chips Ltd, Israel - Giovanna Ober, Riccardo Freddi - DSI GmbH, GermanyRon Nadler, David Guiser, Meira Citroen - Elbit Systems Electrooptics – ELOP Ltd.Israel - Mladen Berekovic from Technische Universitaet Braunschweig, Germany

B5 MISSION OPERATIONS INFRASTRUCTURE—CHAIRED BY PH. LACAN

- 09:30 CCSDS's Cross Support Transfer Services (CSTS)**
Margherita di Giulio – ESA/ESOC, Germany
- 10:00 Operational Simulators moving ahead**
V. Reggestad, M. Sarkarati, K. Widegaard, A. Walsh – ESA/ESOC, Germany

10:30 COFFEE BREAK

B6 MISSION LESSONS LEARNT - CHAIRED BY PH. LACAN

- 11:00 Improvements of Functional Validation by Analysis of Operational Problems**
Nicola Di Nisio, Vemund Reggestad – ESA/ESOC - Joachim Fuchs – ESA/ESTEC, The Netherlands
- 11:30 The Investigation into the Successful Landing on Mars of Beagle 2 (Forensic Software Engineering and Lessons Learnt)**
Roger Ward – Scsys Ltd, United Kingdom

B7 S/C CHECKOUT & TEST - CHAIRED BY R. BLOMMESTIJN

- 12:00 Using IoT Device Technology in Spacecraft Checkout Systems**
Chris Plummer - Space EGSE Ltd., United Kingdom

12:30 LUNCH

B8 CRITICAL S/W TEST ISSV & FAULT CAT. BASED—CHAIRED BY R. BLOMMESTIJN

- 14:00 A testing framework for critical space SW**
Ignacio Fernandez - GTD Sistemas de informacion SA, Spain - Antonio Di Cerbo - Intelligentia srl., Italy - Erik Dehnhardt, Massimo Tipaldi, Bernhard Bruenjes - OHB System AG, Germany

- 14:30 Characterizing verification tools through coding error candidates reported in space flight software**
Christian R. Prause - Deutsches Zentrum f ur Luft- und Raumfahrt e.V. (DLR), Germany—Ralf Gerlich, Rainer Gerlich - Dr. Rainer Gerlich BSSE System and Software Engineering, Germany—Anton Fischer - Etamax space GmbH, Germany

- 15:00 Consolidated View on Space Software Engineering Problems – An empirical study**
N. Silva - CRITICAL Software SA / DEI/CISUC, University of Coimbra, Portugal - M. Vieira - DEI/CISUC, University of Coimbra, Portugal - D. Ricci, D. Cotroneo - Department of Computer and Control Engineering, Federico II University of Naples, Italy

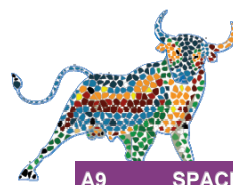
16:00 COFFEE BREAK

16.30 - 18:00 **In Room A: CYBERSPACE SECURITY**
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May 20th, 2015

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Room A

A9 SPACEWIRE USAGE, TESTING & EMULATION CHAIRED BY G. MAGISTRATI

- 08:30 Experimental Results for SpaceWire-D**
Steve Parkes, David Gibson - STAR-Dundee Ltd, United Kingdom - Albert Ferrer Florit - STAR-Dundee Ltd, United Kingdom
- 09:00 A Software Suite for Testing SpaceWire Devices and Networks**
Stuart Mills, Steve Parkes - STAR-Dundee Ltd, United Kingdom
- 09:30 Rapid Real-time SpaceWire Emulation**
Stephen Mudie - STAR-Dundee Ltd., United Kingdom—Steve Parkes, Martin Dunstan - University of Dundee, Space Technology Centre, United Kingdom

A10 SPACEWIRE APPLICATIONS CHAIRED - BY J-L. POUPAT

- 10:00 SpaceWire-based Control System Architecture for the Lightweight Advanced Robotic Arm Demonstrator [LARAD]**
Giuseppe Montano, Elie Allouis – Airbus Defence & Space Ltd, United Kingdom—David Jameux – ESA/ESTEC, The Netherlands

10:30 COFFEE BREAK

A10 SPACEWIRE APPLICATIONS CHAIRED - BY J-L. POUPAT

- 11:00 Demonstrator of SpaceWire/SpaceFibre Network for Mass Memory Applications**
T. Helfers, P. Rastetter, M. Stähle, E. Weih – Airbus DS GmbH, Germany
- 11:30 GR718 – Radiation-Tolerant 18x SpaceWire Router**
Jonas Ekeberg, Sandi Habinc, Fredrik Johansson, Fredrik Sturesson, Martin Simlask, Francisco Hernandez - Cobham Gaisler, Sweden—Steven Redant, Kurt Stinkens, Geert Thys, Jagadeesa Das Arul Mahesh – IMEC, Belgium—Martin Suess - ESA/ESTEC, The Netherlands

A11 SPACE FIBRE - CHAIRED BY R. TRAUTNER

- 12:00 SpaceFibre: The Standard, Simulation, IP Cores and Test Equipment**
Steve Parkes, Chris McClements, David McLaren - STAR-Dundee Ltd, United Kingdom - Albert Ferrer Florit, Alberto Gonzalez Villafranca - STAR-Dundee Ltd, United Kingdom

12:30 LUNCH

A11 SPACE FIBRE - CHAIRED BY R. TRAUTNER

- 14:00 VHiSSI: experimental spacefibre Asic**
Alberto Gonzalez Villafranca, Albert Ferrer Florit - STAR-Dundee Ltd., United Kingdom - Space Technology Centre, University of Dundee, United Kingdom

A12 TT ETHERNET NETWORKS - CHAIRED BY P. CORMERY

- 14:30 Deterministic ethernet for space applications**
Christian Fidi - TTTech Computertechnik AG, Austria - Bernd Wolff - Airbus Defence and Space GmbH, Germany
- 15:00 Development of a deterministic ethernet building blocks for space applications**
Christian Fidi, Mirko Jakovljevic - TTTech Computertechnik AG, Austria
- 15:30 Automatic implementation of TTEthernet-based time-triggered avionics applications**
Raul Adrian Gorcitz, David Monchaux – CNES, France - Thomas Carle, Dumitru Potop-Butucaru, Yves Sorel - INRIA - Institut National de Recherche en informatique et en Automatique, France - David Lesens – Airbus Defence & Space ST, France

16:00 COFFEE BREAK

A13 CAN BUS - CHAIRED BY P. LE MEUR

- 16:30 Can in space status and future after ECSS-E-ST-50-15C release**
Chris Taylor, Gianluca Furano, Alberto Valverde-Carretero, Kostas Marinis, Giorgio Magistrati, Luca Bolognino, King Lam Richard Jansen – ESA/ESTEC, The Netherlands—Maurizio Caramia - Thales Alenia Space, France—Angel Grau Llovera – Thales Alenia Space, Italy—Jean Dalenq, Francois Vinet – Airbus Defence & Space SAS, France

A14 FIBRE SENSOR - CHAIRED BY P. LE MEUR

- 17:00 Fiber Optic Sensing. Prototype results**
Jesús Ortiz Martín, José González Torres –, Silvia Abad - Airbus Defence & Space Crisa, Fibersensing, Spain

May 21st, 2015



Room B

B9 PROCESSING TIMING ANALYSIS & CONTROL - CHAIRED BY C. JÖRGENSEN

- 08:30 Towards Making a LEON3 Multicore Compatible with Probabilistic Timing Analysis**
Carles Hernandez, Jaume Abella - Barcelona Supercomputing Center, Spain - Francisco J. Cazorla - Barcelona Supercomputing Center, Spanish National Research Council (IIIA-CSIC), Spain - Jan Andersson, Andrea Gianarro - Cobham Gaisler, Sweden
- 09:00 Measurement Based Probabilistic Timing Analysis_ From Academia to Space Industry**
Philippa Ryan Conmy, Mark Pearce- Rapita Systems Ltd., United Kingdom - Marco Ziccardi, Enrico Mezzetti, Tullio Vardanega - Department of Mathematics University of Padua, Italy - Jan Anderson, Andrea Gianarro - Cobham Gaisler, Sweden - Carles Hernandez - Barcelona Supercomputing Center, Spain - Francisco J. Cazorla - Spanish National Research Council (IIIA-CSIC), Spain
- 09:30 Cross-Domain Certification for Hybrid Worst Case Execution Time Analysis_Current and Future Approaches**
Philippa Ryan Conmy, Guillem Bernat, Zoë Stephenson, David George - Rapita Systems Ltd., United Kingdom
- 10:00 Automatic verification of timing constraints for safety critical space systems**
Javier Fernandez Salgado, Pablo Parra, Oscar Polo - Space Research Group, Universidad de Alcala, Spain - Guillem Bernat - Rapita Systems Ltd, United Kingdom

10:30 COFFEE BREAK

B10 FUTURE LAUNCHER ARCHITECTURE & TECHNOLOGY - CHAIRED BY PH. LACAN

- 11:00 New Technologies for Future Launchers Avionics**
Philippe Miramont, David Monchaux, Bruno Regnier - Centre National d'Etudes Spatiales, France
- 11:30 Generic software architecture for launchers**
Emilien Carre, Philippe Gast, Emmanuel Hiron, Alain Leblanc, David Lesens, Emmanuelle Mescam, Pierre Moro – Airbus Defence & Space ST, France
- 12:00 On-board processor and network maturation for Ariane 6**
Rémi Clavier, Pierre Sautereau, Jérémie Sangaré, Benjamin Disson – Airbus Defence and Space SAS ST, France

12:30 LUNCH

B11 COMMUNICATION LINK ANALYSIS & FILE TRANSFER - CHAIRED BY CH. TAYLOR

- 14:00 A TT&C Performance Simulator for Space Exploration and Scientific Satellites – Architecture and Applications**
G. Donà, M. Faletta – Thales Alenia Space Italy, Italy
- 14:30 IP based ground_space communication for Columbus**
Sven Rakers, Dieter Arndt, Frank Bade, Carol Brüning, Marc-Julien Etti, Frank Lehmann, Christoph Massau, Thomas Mrotzeck, Ludwig Vogeler, Gerd Winkelmann - Airbus Defence+Space, Germany - Per Bonde, Giuliano Canovai, Andres Grop, John Nolan, Sara Pastor - ESA/ESTEC, The Netherlands
- 15:00 CFDP evolutions and file based operations**
*Alberto Valverde, Chris Taylor, Giorgio Magistrati, Elena Maiorano, Cyril Colombo - ESA/ESTEC, The Netherlands
Colin Haddow – ESA/ESOC, Germany*

B12 UAV ON TITAN - CHAIRED BY R. GERLICH

- 15:30 MDO, WIND tunnel and flight tests of UAVs FOR TITAN exploration**
Davide Bonetti, Antonio Latorre Sierra, Jordi Freixa Mallol - DEIMOS Space S.L.U., Spain - Federico Martín de la Escalera Cutillas - Aernnova Engineering Solutions Iberica SA, Spain - Bugallo Miguens - SCR Sistemas de Control Remoto S.L., Spain - Miguel Ángel Gonzalez, Ana Isabel Moreno, Artur Andrzej Jarzabek - Escuela Técnica Superior de Ingenieros Aeronáuticos. Pza, Spain

16:00 COFFEE BREAK

B12 UAV ON TITAN - CHAIRED BY R. GERLICH

- 16:30 Validation of inertial and imaging navigation techniques for space applications with UAVS**
J. Montaño, M. Wis, J. A. Pulido, A. Latorre - Deimos Space, Spain - P. Molina, E. Fernández, E. Angelats ? I. Colomina - Centre Tecnològic de Telecomunicacions de Catalunya (CTTC), Spain
- 17:00 FLYCON-R: wireless integrated communication and navigation system**
Jose Maria Palomo, Sergio Gómez de Agüero, Antonio Latorre, Antonio Fernandez - DEIMOS Space S.L.U., Spain—Andrei Tarziu, Fernando Pina - Deimos Space S.R.L, România - Mugurel Balan - Institute of Space Science, Romania—Manuel Sanchez Gestido, Paolo Concarì – ESA/ESTEC, The Netherlands
- 17:30 Application of h-infinity fault detection to model-scale autonomous aircraft**
J. F. Vasconcelos, P. Rosa, Murray Kerr, Antonio Latorre Sierra, Cristina Recupero, Lucía Hernández - Elecnor Deimos, Spain

May 21st, 2015

LOCATION

PULLMAN SKIPPER

Avinguda Litoral, 10

08005 Barcelona

Spain

Front desk: +34 932 21 65 65



REGISTRATION

- Advance:** Please complete the electronic Conference Registration Form (<http://eurospace.org/dasia-2015.aspx>) and send it to Eurospace before May 1st, 2015.
- On site:** The Conference Secretariat will open on May 19th, 2015 for late registration, documentation and information, and will remain in operation throughout the Conference

CONFERENCE FEES

A Conference fee of **900 €** is requested from all participants, including speakers and chairmen. This fee will include lunches, coffee breaks, reception and proceedings.

The fee for a Vendor Display is **€2 000** and includes one attendance fee for the Conference.

PAYMENT

Before the Conference:

- By cheque:** in Euro to the order of EUROSPACE payable in a French Bank to be sent to:
EUROSPACE – 15/17, avenue de Ségur – 75007 PARIS, France.
- By bank transfer:** in Euro to **EUROSPACE – IBAN: FR76 3006 6100 9100 0103 6680 245 – B.I.C: CMCIFRPP**
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Charges on bank orders shall be borne by the sender

- In cash or credit card:** In Euro **only** by cash, with a **VISA** or a **MASTERCARD** (AMERICAN EXPRESS will not be accepted)

CANCELLATIONS

Refunds of 50% will be made if a written request is received **before May 1st, at the latest.**
No refunds will be made for cancellations received after this date, but substitutions are possible.

HOTEL RESERVATIONS

A block reservation has been made at special Conference rates at PULLMAN SKIPPER hotel.

It is recommended to reserve hotel accommodation **very rapidly** via the link provided in the **Eurospace website** (<http://eurospace.org/dasia-2015.aspx>), as May-June is the peak season for business in Barcelona.

After March 15th, reservation will be on normal request and upon availability.

Important notice: The application of DASIA 2015 negotiated rate(s) is subject to the use of the order form specifically provided by the Pullman Skipper hotel available through the above link. **Please inform your travel agent if necessary.**

Once the booking confirmed, please contact the hotel directly regarding any subsequent question/change.

LANGUAGES

English will be the working language of the Conference. The time allocated for each presentation will include a discussion at the end

TRAVEL & BANK FACILITIES

Travel and **exchange** arrangements can be made by the Concierge of the Hotel.

PROCEEDINGS

The Proceedings will be published after the Conference :

On a CD Rom:

And via Eurospace server:

Authors are requested to deliver the pdf version of their final paper(s) **by June 1st, 2015 at the latest**, via the following address exclusively: **dasia2015@eurospace.org**

CHAIRMAN & PROGRAMME COMMITTEE

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A. Rossignol, Airbus Defence & Space France - Satellites

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R. Ward, SciSys Ltd

